



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Lawrence S. Uzelac, et al.      Art Unit : Unknown  
Serial No. : 10/625,584      Examiner : Unknown  
Filed : July 22, 2003  
Title : METHOD AND APPARATUS FOR REDUCING SUB-THRESHOLD OFF  
CURRENT FOR A REAL TIME CLOCK CIRCUIT DURING BATTERY  
OPERATION

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Copies of the references listed on the attached form PTO-1449 are enclosed.

This statement is being filed within three months of the filing date of the application or before the receipt of a first Office Action on the merits. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date:

Oct. 28, 2004

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Substitute Form PTO-1449 (Modified) <b>Information Disclosure Statement</b> by Applicant (Use several sheets if necessary)  (37 CFR §1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-109002	Application No. 10/625,584
	Applicant Lawrence S. Uzelac, et al.		
	Filing Date July 22, 2003	Group Art Unit	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,838,171	11/17/1998	Davis			
	AB	5,909,140	06/01/1999	Choi			
	AC	5,917,365	06/1999	Houston, Theodore W.			
	AD	6,191,470	02/2001	Forbes et al.			
	AE	6,307,233	10/2001	Awaka et al.			
	AF	6,373,321	04/2002	Yamauchi et al.			
	AG	6,380,798	04/2002	Mizuno et al.			
	AH	6,715,090B1	03/30/2004	Tosuka et al.			
	AI	2004/0158756 A1	08/12/2004	Tosuka et al.			

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AJ	10189884A	07/1998	Japan			Abstract Only	
	AK	10-1998-0004940	03/30/1998	Korea			Abstract Only	
	AL	10-1999-0078182	10/25/1999	Korea			Abstract Only	
	AM							
	AN							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AO	Anonymous, <i>Diode Battery Back-Up Switch</i> , IBM TECHNICAL DISCLOSURE BULLETIN, VOL. 30, NO. 2, July 1, 1987, pg 895
	AP	Muhammad M. Khellah and M.I. Emasry, <i>Power Minimization of High-Performance Submicron CMOS Circuits Using a Dual-V<sub>d</sub> Dual-V<sub>th</sub> (DVDV) Approach</i> , PROCEEDINGS 1999 INTERNATIONAL SYMPOSIUM ON LOW POWER ELECTRONICS AND DESIGN, August 1999, ACM, pp. 106-108
	AQ	K. Roy, Leakage power reduction in low-voltage CMOS designs, Electronics, Circuits and Systems, 1998 IEEE International Conference, Vol. 2, 1998, pgs. 167-173
	AR	Office Action received in Korean Patent Application No. 10/2002/7008020
	AS	Office Action received in Chinese Patent Application No. 00817542.X
Examiner Signature		Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		

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Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AT	International Preliminary Examination Report received in PCT/US00/42597
	AU	International Search Report received in PCT/US00/42597

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	